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COMPUTER SCIENCE & ENGINEERING

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802	4	50	100	150	EC 803	Opto	Electronics & Communication	4	50	100	150	EC	804	Elective II	4	50	100	150	EC	805	Project Work	14	300	300	EC	806	Viva-voce	100	100

An Efficient Design of Frame Synchronoinization in FPGA

May 01, 2015 · Sri Ventakeshwara College of Engineering, Chitoor YMurali Mohan Babu, PhD, MTech Professor, Sri Ventakeshwara College of Engineering, Chitoor Framesynchronization is obtained by inserting in series (eg at the beginning of each frame), or inparallel (ie on a separate sync

channel), a frame sync code (FSC)

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common example of full-duplex communication is the telephone network When two people are communicating by a telephone line, both can talk and listen at the same time The full-duplex mode is used when communication in both directions is required all the time The capacity of the channel, however, must be divided between the two directions

ISSN (Online) 2394-6849 Engineering (IJERECE) Vol 5, Issue ...

International Journal of Engineering Research in Electronics and Communication Engineering (IJERECE) Vol 5, Issue 4, April 2018 Advanced Vehicle Security System on FPGA [1] KMurali Chandra Babu, [2] DrPAHarsha Vardhini [1], [2] Vignan Institute of Technology and Science, Deshmukhi

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21 Y Murali Mohan Babu ECE A New Approach to Radar Image Despeckling using Undecimated Wavelet Transform Based Block Matching 3D Algorithm for The Department of Electronics and Communication Engineering, JNTUACEA organized a National Level Students' Technical Symposium, e-MERGE' on 17th & 18th of March 2016 The innovations, creativity

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